



TIM internal peripheral



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- [\(cur | prev\)09:46, 29 March 2021 m . . \(8,226 bytes\) \(-225\) \[Reviewed: 13:21, 30 March 2021 | | \[Expert: Approved, Technical writer: Approved, Maintainer: Approved\]\]](#)
- [\(cur | prev\)11:11, 5 January 2021 m . . \(8,451 bytes\) \(+4\) \[Reviewed: 16:48, 5 January 2021 | | \[Expert: Approved, Technical writer: Approved, Maintainer: Approved\]\]](#)
- [\(cur | prev\)14:28, 16 January 2020 m . . \(8,194 bytes\) \(-135\) \[Reviewed: 14:30, 16 January 2020 | | \[Expert: Approved, Technical writer: Approved, Maintainer: Approved\]\]](#)
- [\(cur | prev\)14:52, 29 January 2019 m . . \(8,329 bytes\) \(+11\) \[Reviewed: 08:18, 12 February 2019 | \]](#)