



Revision history of "STM32MP15 peripherals overview"

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- [\(cur | prev\)07:17, 2 August 2021 m . . \(8,737 bytes\) \(-1\) \[Reviewed: 07:18, 2 August 2021 | \]](#)
- [\(cur | prev\)08:46, 4 June 2021 m . . \(8,637 bytes\) \(0\) . . \(â Internal peripherals overview\) \[Reviewed: 09:03, 4 June 2021 | \]](#)
- [\(cur | prev\)13:48, 22 January 2020 m . . \(8,637 bytes\) \(+21\) \[Reviewed: 16:15, 22 January 2020 | \] \[Expert: Aproved, Technical writer: Approved, Maintainer: Approved\]](#)
- [\(cur | prev\)09:02, 15 October 2019 m . . \(8,616 bytes\) \(-134\) \[Reviewed: 09:03, 15 October 2019 | \] \[Expert: Aproved, Technical writer: Approved, Maintainer: Approved\]](#)
- [\(cur | prev\)15:25, 27 August 2019 m . . \(8,750 bytes\) \(-73\) \[Reviewed: 15:38, 27 August 2019 | \]](#)
- [\(cur | prev\)12:13, 23 April 2019 m . . \(8,757 bytes\) \(+59\) \[Reviewed: 12:16, 23 April 2019 | \]](#)
- [\(cur | prev\)15:26, 13 February 2019 m . . \(8,698 bytes\) \(-19\) \[Reviewed: 14:36, 15 February 2019 | \]](#)