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# STM32MP15 microprocessor

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# Contents



Stable: 25.10.2019 - 08:43 / Revision: 25.10.2019 - 08:43

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A quality version of this page, accepted on *25 October 2019*, was based off this revision.

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- the supported **software distributions**, that can be dowloaded into the STM32MP15 device.

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## 2 Part number codification

The table below shows the STM32MP15 microprocessor different part numbers available, together with their corresponding internal peripherals, security options and packages.

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| | STM32MP151 Access line | STM32MP153 Foundation line | STM32MP157 Advanced line |
|---|---|---|---|
| Cortex-A7 | Single | Dual | Dual |
| Cortex-M4 | Yes | Yes | Yes |
| GPU | No | No | Yes |
| Display | TFT | TFT | TFT/DSI |
| CAN | No | Yes | Yes |

- Security:

| | |
|---|---|
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| STM32MP15xC | Secure boot + Cryptography (CRYP) |

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| | |
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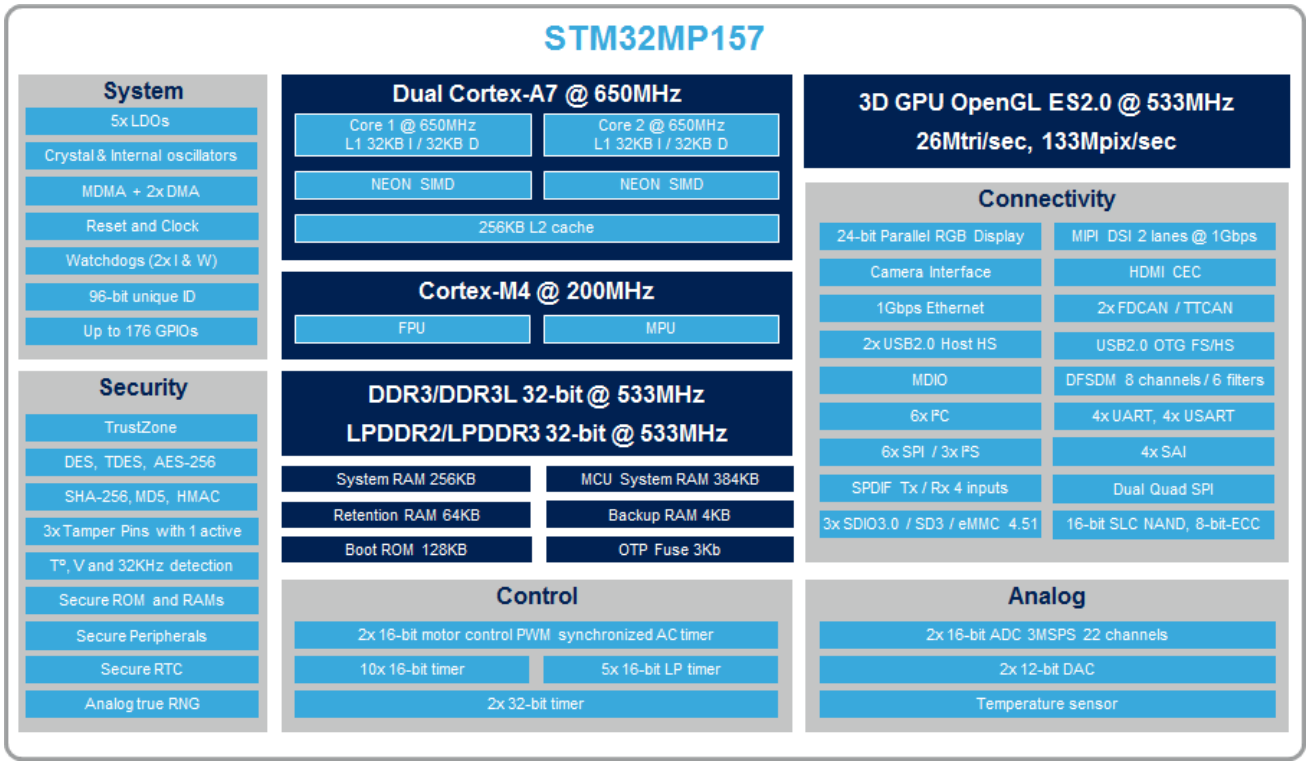

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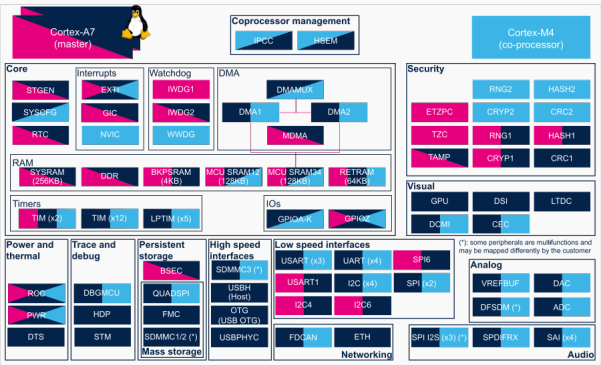

Internal peripherals

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Stable: 12.02.2020 - 16:40 / Revision: 12.02.2020 - 16:37

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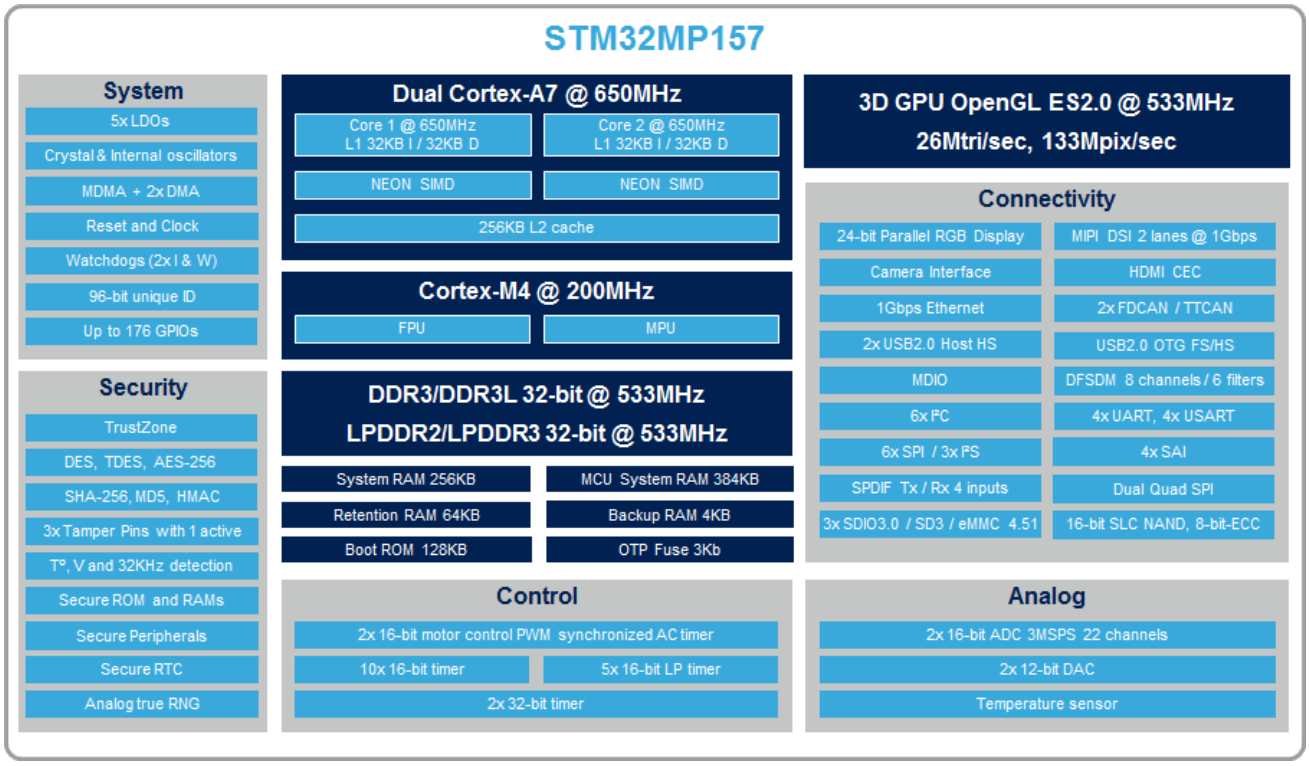

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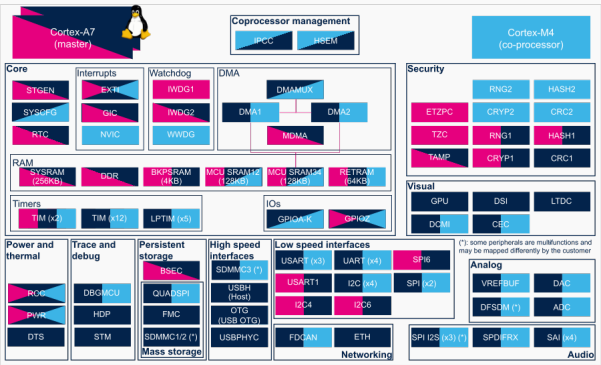

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Stable: 17.06.2020 - 15:27 / Revision: 16.01.2020 - 13:36

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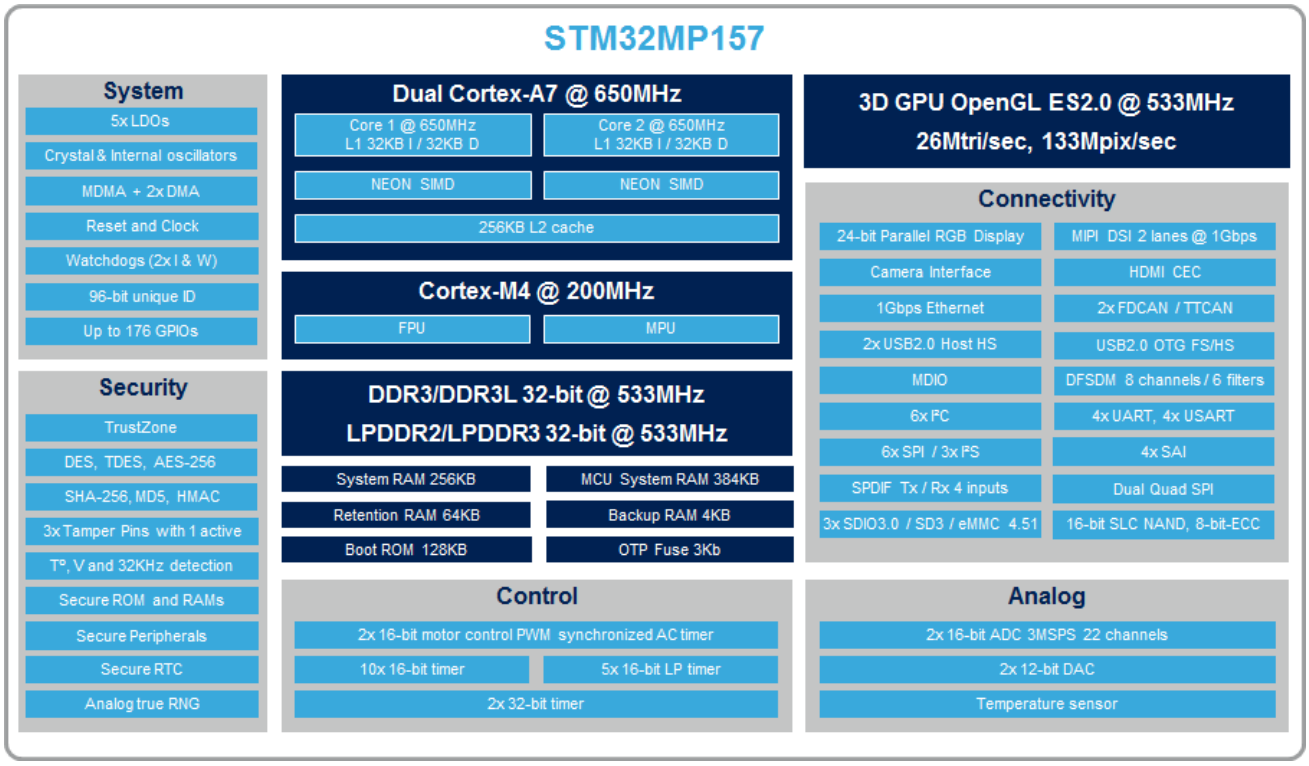

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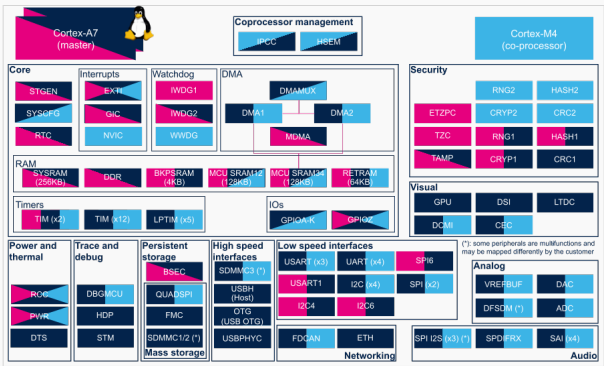


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## Subcategories

This category has the following 2 subcategories, out of 2 total.

### A

- Android distribution (2 P)

### S

- STM32Cube MPU Package (3 P)

Stable: 08.06.2020 - 07:20 / Revision: 18.05.2020 - 15:26

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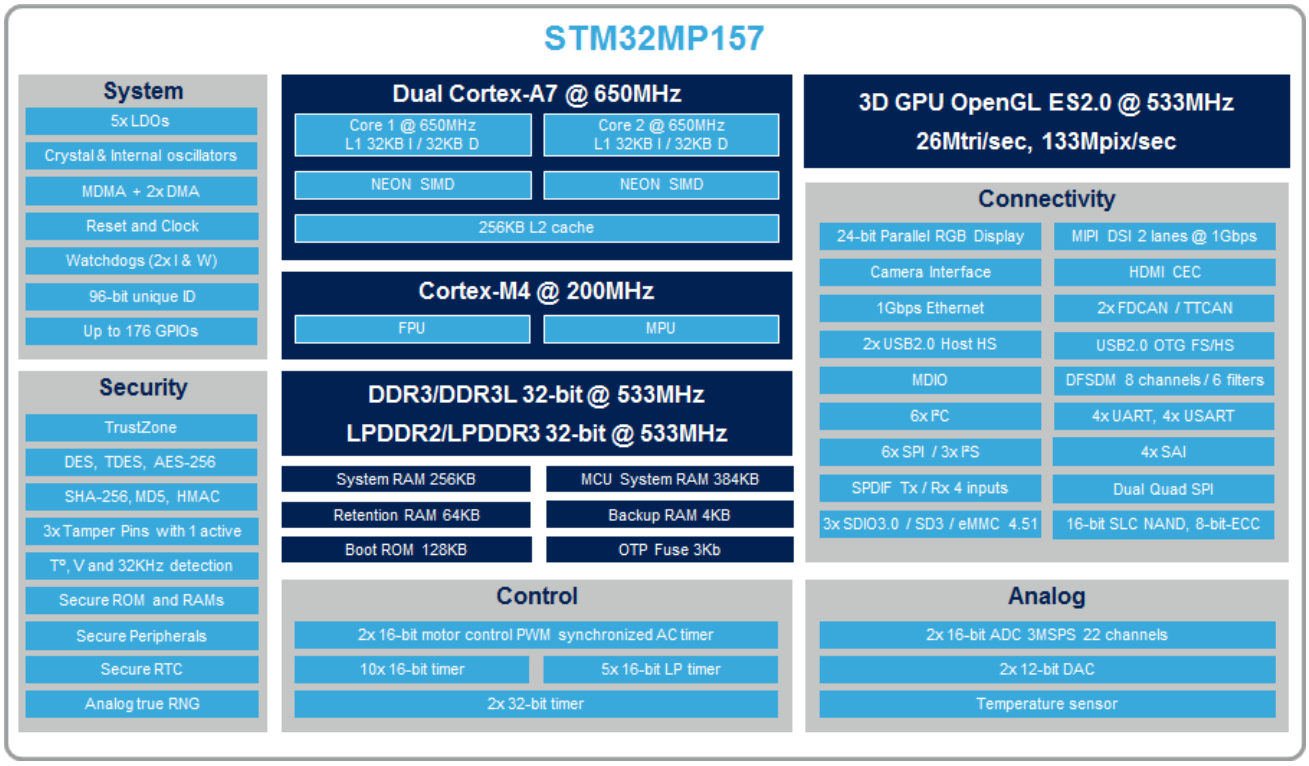

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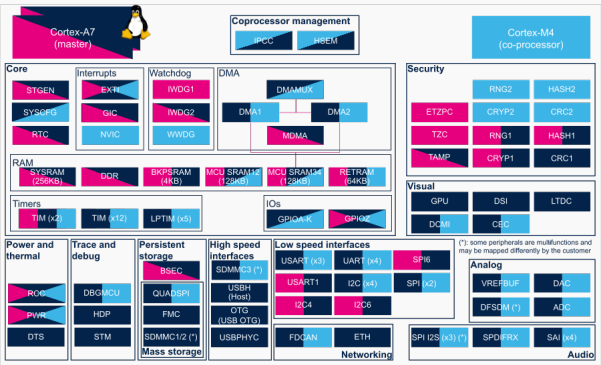


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Stable: 22.01.2020 - 16:15 / Revision: 22.01.2020 - 13:48

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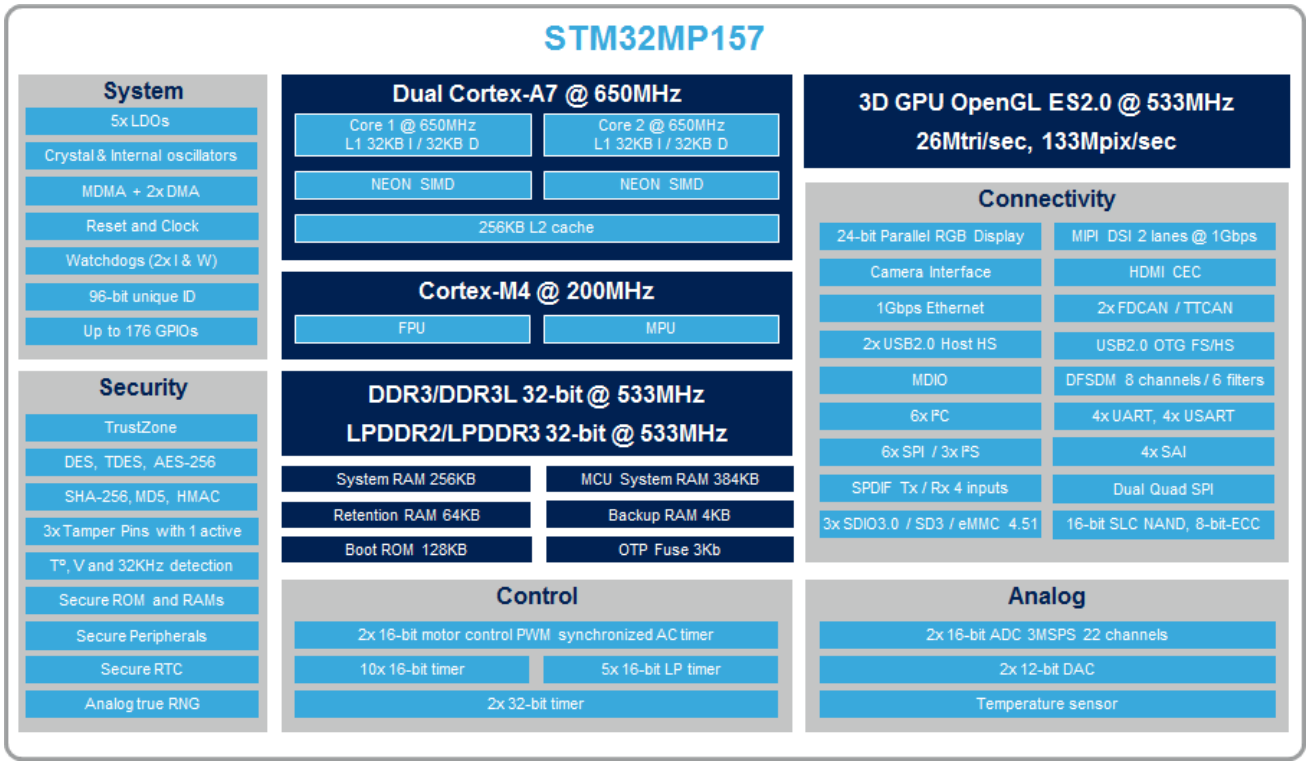

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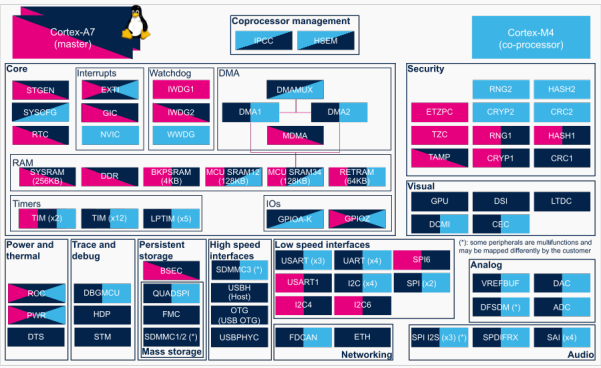

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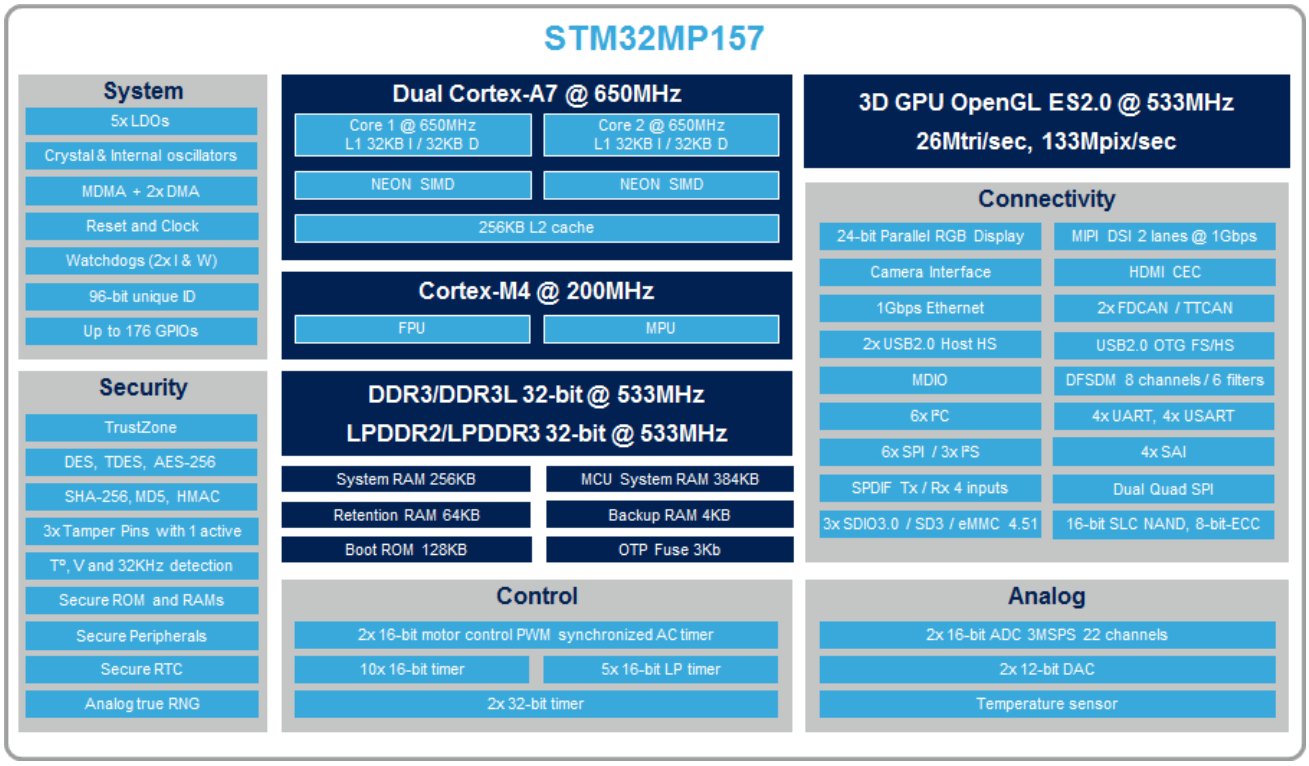

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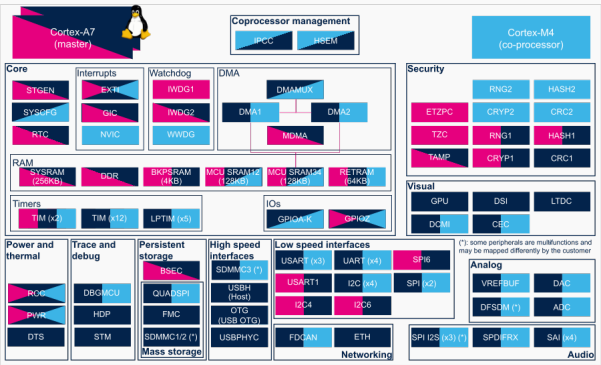

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Stable: 15.04.2020 - 14:24 / Revision: 15.04.2020 - 14:21

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| | |
|---|---|
| STM32MP15xA | Basic |
| STM32MP15xC | Secure boot + Cryptography (CRYP) |

- Packages:

| | |
|---|---|
| STM32MP15xxAA | TFBGA448 18x18 |
| STM32MP15xxAB | LFBGA354 16x16 |
| STM32MP15xxAC | TFBGA361 12x12 |
| STM32MP15xxAD | TFBGA257 10x10 |

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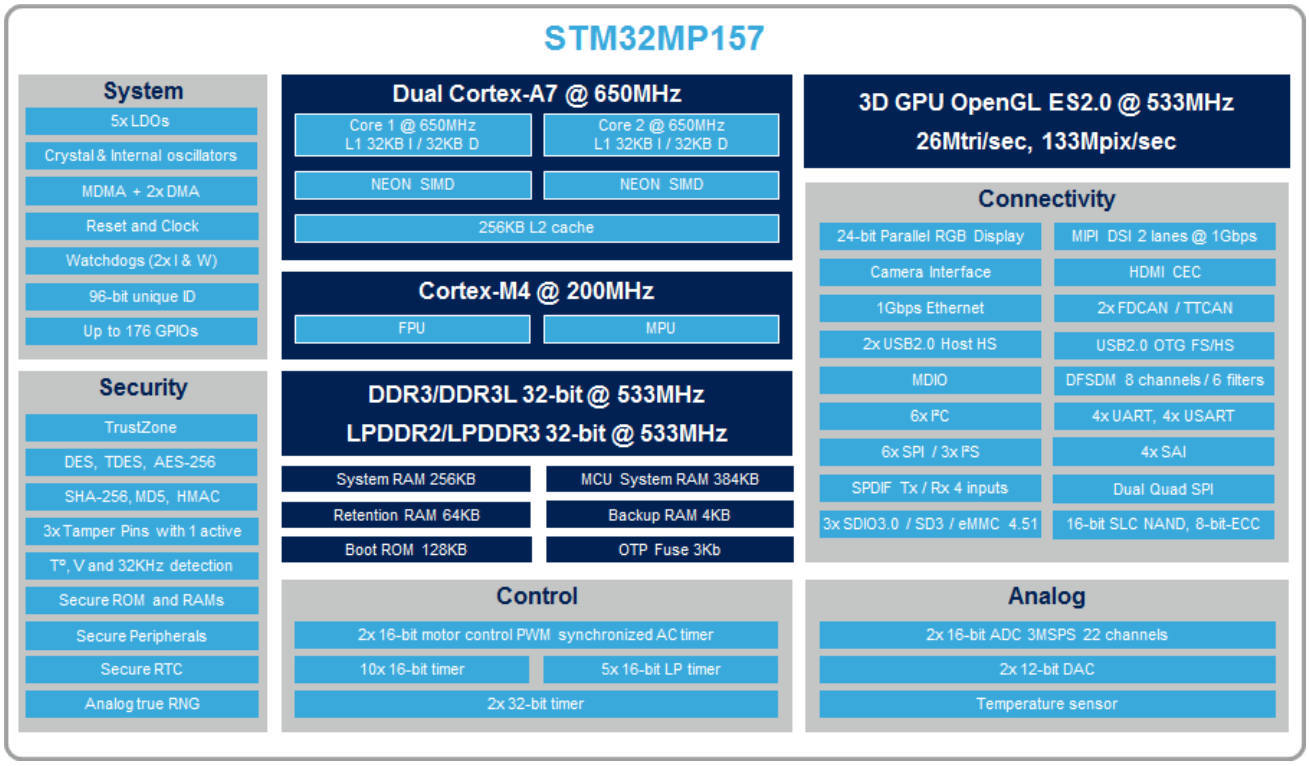
# 3 Block diagrams

Here below is the STM32MP157 block diagram offering the richest features set of the STM32MP15 microprocessor.



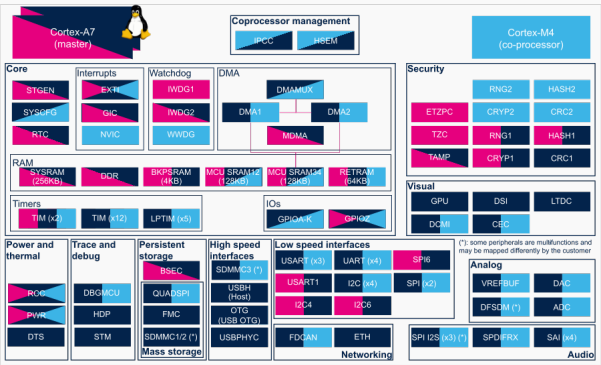
# 4 Technical documentation

- STM32MP15 Reference Manual: device and internal peripheral user specifications
- STM32MP15 Datasheet: package and pinout descriptions

# 5 Internal peripherals

STM32MP15 peripherals overview article gives a description of all the internal peripherals available on STM32MP15 devices, with direct links to the articles where you can find:

- an overview of each peripheral
- the list of instances available for each peripheral type,
- information on the way each instance can be shared between Arm® Cortex®-A7 and Cortex®-M4 cores,
- direct links to the software frameworks used to control the peripheral from different Arm® cores and security modes such as Cortex®-A7 non secure, Cortex®-A7 secure or Cortex®-M4 (non secure).



Internal peripherals

# 6 How to get further with STM32MP15 ecosystem

## 6.1 Boards

The list of boards that integrate STM32MP15 devices can be found in STM32MP15 boards article.

## 6.2 Supported software distributions

| | |
|---|---|
| **STM32MPU Embedded Software distribution** | **STM32MPU Embedded Software distribution for Android** |

Click the links above to find information on:

- Distribution composition and associated software architecture
- Associated release notes

# 7 References

- STM32 MCU family

Graphics Processing Units

Microcontroller Unit (MCUs have internal flash memory and are intended to operate with a minimum amount of external support ICs. They commonly are a self-contained, system-on-chip (SoC) designs.)

Display Serial Interface (MIPI® Alliance standard)

Controller Area Network (robust bus mainly used for automotive applications)