



STM32MP15 RAM mapping



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- [\(cur | prev\)17:08, 5 January 2021 m . . \(9,797 bytes\) \(-263\) . . \(â MCU SRAM4\) \[Reviewed: 17:13, 5 January 2021 | \]](#)
- [\(cur | prev\)09:38, 12 October 2020 m . . \(9,938 bytes\) \(-30\) \[Reviewed: 09:38, 12 October 2020 | \]](#)
- [\(cur | prev\)09:05, 25 September 2020 m . . \(9,968 bytes\) \(-1\) \[Reviewed: 09:06, 25 September 2020 | | \[Expert: Aproved, Technical writer: Approved, Maintainer: Approved\]\]](#)
- [\(cur | prev\)07:30, 23 June 2020 m . . \(9,969 bytes\) \(-51\) \[Reviewed: 07:31, 23 June 2020 | | \[Expert: Aproved, Technical writer: Approved, Maintainer: Approved\]\]](#)

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