



SPDIFRX internal peripheral



Contents

1. SPDIFRX internal peripheral	
2. STM32MP15 resources	
3. ALSA overview	
4. STM32CubeMP1 architecture	
5. STM32CubeMX	
6. SPDIFRX Linux driver	
7. Soundcard configuration	
8. SPDIFRX device tree configuration	
9. STM32MPU Embedded Software architecture overview	
10. How to assign an internal peripheral to a runtime context	
11. Device tree	



SPDIFRX internal peripheral

Stable: 04.02.2020 - 16:00 / Revision: 04.02.2020 - 15:52

Contents

1 Article purpose	3
2 Peripheral overview	3
2.1 Features	3
2.2 Security support	4
3 Peripheral usage and associated software	4
3.1 Boot time	4
3.2 Runtime	4
3.2.1 Overview	4
3.2.2 Software frameworks	4
3.2.3 Peripheral configuration	4
3.2.3.1 Configuration in Cortex-A7 non-secure software	5
3.2.3.2 Arm [®] Cortex [®] -M4 software configuration	5
3.2.4 Peripheral assignment	5
4 How to go further	6
5 References	6

1 Article purpose

The purpose of this article is to:

- briefly introduce the SPDIFRX peripheral and its main features
- indicate the level of security supported by this hardware block
- explain how each instance can be allocated to the three runtime contexts and linked to the corresponding software components
- explain how to configure the SPDIFRX peripheral.

2 Peripheral overview

The **SPDIFRX** peripheral, is designed to receive an S/PDIF flow compliant with IEC-60958 and IEC-61937. The SPDIFRX receiver provides two separated paths to retrieve the audio data and the user and channel information.

2.1 Features

Refer to the [STM32MP15 reference manuals](#) for the complete feature list, and to the software components, introduced below, to see which features are implemented.



2.2 Security support

The SPDIFRX is a **non secure** peripheral.

3 Peripheral usage and associated software

3.1 Boot time

The SPDIFRX is not used at boot time.

3.2 Runtime

3.2.1 Overview

The SPDIFRX instance can be allocated to:

- the Arm® Cortex®-A7 non-secure for use in Linux with ALSA framework
- the Cortex-M4 for use in STM32Cube with STM32Cube SPDIFRX driver

Chapter #Peripheral assignment exposes which instance can be assigned to which context.

3.2.2 Software frameworks

Do	Peri	Software frameworks		Comment
mai Cortex -A7 non-secure (OPE) TE E)	Cor tex -A7 no n- sec ure (Li nux)	Cortex-M4 (STM32Cube)		
Au dio	SP DI FR X	ALSA framework	STM32Cube SPDIFRX driver	

3.2.3 Peripheral configuration

The configuration is applied by the firmware running in the context to which the peripheral is assigned. The configuration can be done alone via the STM32CubeMX tool for all internal peripherals, and then manually completed (particularly for external peripherals), according to the information given in the corresponding software framework article.

3.2.3.1 Configuration in Cortex-A7 non-secure software

When the Arm® Cortex®-A7 core operates in non-secure access mode, the SPDIFRX is controlled by the Linux kernel framework. Refer to the SPDIFRX Linux driver to drive the SPDIFRX through Linux kernel ALSA framework. Refer to Soundcard configuration and SPDIFRX device tree configuration to configure the SPDIFRX through Linux kernel device tree [1].

3.2.3.2 Arm® Cortex®-M4 software configuration

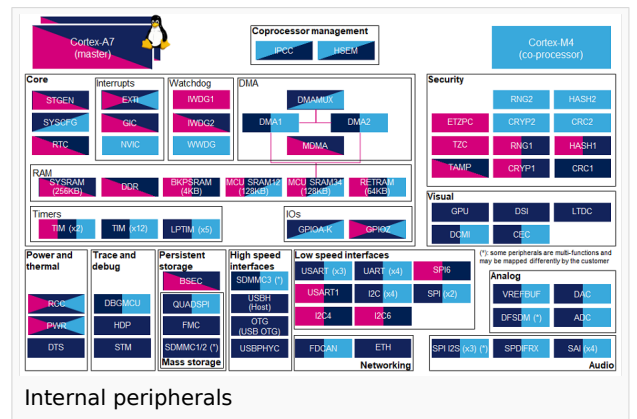
3.2.4 Peripheral assignment

Check boxes illustrate the possible peripheral allocations supported by STM32 MPU Embedded Software:

- means that the peripheral can be assigned () to the given runtime context.
- is used for system peripherals that cannot be unchecked because they are statically connected in the device.

Refer to How to assign an internal peripheral to a runtime context for more information on how to assign peripherals manually or via STM32CubeMX.

The present chapter describes STMicroelectronics recommendations or choice of implementation. Additional possibilities might be described in STM32MP15 reference manuals.



Do	Per	Runtime allocation			Comme
ma	in	Cortex-A7 non-secure (Linux)	Cortex-M4 (STM32Cube)		
in	era				
	te				
	x-				
	A				
	7				
	se				
	cu				
	re				
	(
	O				
	P				
	T				
	E				
	E)				
A	SP				Assign
u	D				ment
	I				(singl
	F				e



Do	Per	Runtime allocation			Comme
ma	iph	SPDIFRX			choice
in	era)
o	I				

4 How to go further

The STM32H7 SPDIFRX training ^[2], introduces the STM32 S/PDIF Receiver interface on the STM32H7. This training also applies to the STM32 MPU SPDIFRX internal peripheral.

5 References

- Device tree
- STM32H7 SPDIFRX training

Sony/Philips Digital Interface Format (Protocol (IEC-60958))

Open Portable Trusted Execution Environment

Microprocessor Unit

Permission error

Stable: 21.02.2020 - 08:59 / Revision: 14.02.2020 - 10:13

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 04.02.2020 - 07:47 / Revision: 04.02.2020 - 07:36

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 21.02.2020 - 08:39 / Revision: 04.02.2020 - 15:22

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer



Permission error

Stable: 31.01.2020 - 13:04 / Revision: 31.01.2020 - 13:02

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 22.01.2020 - 15:46 / Revision: 22.01.2020 - 10:02

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 27.01.2020 - 10:13 / Revision: 27.01.2020 - 10:12

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 06.02.2020 - 15:03 / Revision: 06.02.2020 - 15:01

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 15.10.2019 - 11:55 / Revision: 15.10.2019 - 11:55

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 22.01.2020 - 16:08 / Revision: 22.01.2020 - 10:33

You do not have permission to read this page, for the following reason:



The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

Permission error

Stable: 04.02.2020 - 07:47 / Revision: 04.02.2020 - 07:34

You do not have permission to read this page, for the following reason:

The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer