



Internal peripherals assignment table template

Internal peripherals assignment table template

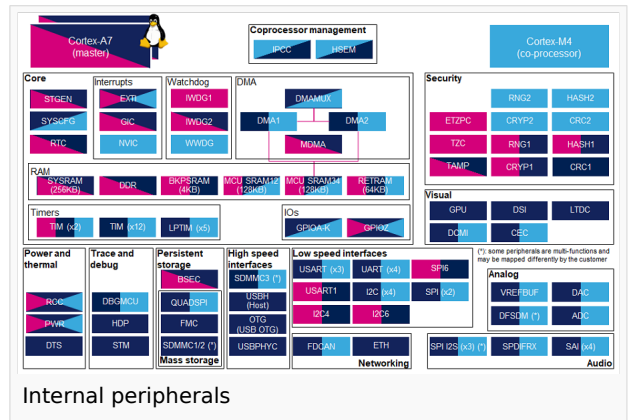
Stable: 21.01.2020 - 15:55 / Revision: 21.01.2020 - 14:37

Check boxes illustrate the possible peripheral allocations supported by STM32 MPU Embedded Software:

- **â** means that the peripheral can be assigned (**â**) to the given runtime context.
- **â** is used for system peripherals that cannot be unchecked because they are statically connected in the device.

Refer to How to assign an internal peripheral to a runtime context for more information on how to assign peripherals manually or via STM32CubeMX.

The present chapter describes STMicroelectronics recommendations or choice of implementation. Additional possibilities might be described in STM32MP15 reference manuals.



Do ma in	Pe ri ph er al	Runtime allocation			Comme nt
		Instance	Cortex-A7 secure (OP-TEE)	Cortex-A7 non-secure (Linux)	

Open Portable Trusted Execution Environment