



Internal peripherals assignment table template



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```
<onlyinclude> [[File: STM32MP1IPsOverview.png|link=STM32MP15_peripherals_overview|thumb|Internal peripherals]] '''Check boxes''' illustrate the possible peripheral allocations supported by [[STM32MPU_Embedded_Software_architecture_overview|STM32 MPU Embedded Software]]: * <span title="assignable peripheral" style="font-size:21px"></span> means that the peripheral can be assigned (<span title="assigned peripheral" style="font-size:21px"></span>) to the given runtime context. * <span title="system peripheral" style="font-size:21px"></span> is used for system peripherals that cannot be unchecked because they are statically connected in the device. Refer to [[How to assign an internal peripheral to a runtime context]] for more information on how to assign peripherals manually or via [[STM32CubeMX]]. <br /> The present chapter describes STMicroelectronics recommendations or choice of implementation. Additional possibilities might be described in [[STM32MP15 resources#Reference manuals|STM32MP15 reference manuals]]. { | class="contenttable" style="text-align: center;" width=80% | rowspan="2" bgcolor="#B9C4CA" width=12% | '''Domain''' | rowspan="2" bgcolor="#B9C4CA" width=12% | '''Peripheral''' | colspan="4" bgcolor="#B9C4CA" width=51% | '''Runtime allocation''' | rowspan="2" bgcolor="#B9C4CA" width=25% | '''Comment''' |- | bgcolor="#90989E" width=15% style="color: white;" | <small>Instance</small> | bgcolor="#D4007A" width=12% style="color: white;" | <small>Cortex-A7<br />secure<br /> (OP-TEE)</small> | bgcolor="#002052" width=12% style="color: white;" | <small>Cortex-A7<br />non-secure<br /> (Linux)</small> | bgcolor="#39A9DC" width=12% style="color: white;" | <small>Cortex-M4<br /><br /> (STM32Cube)</small> |- </onlyinclude> } <noinclude> [[Category:Sub-articles]] </noinclude>
```

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