HDP internal peripheral

Contents

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A quality version of this page, approved on *15 February 2019*, was based off this revision.

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- briefly introduce the **HDP** peripheral (hardware debug port) and its main features
- indicate the level of security supported by this hardware block
- explain how each instance can be allocated to the three runtime contexts and linked to the corresponding software components
- explain, when needed, how to configure the HDP peripheral.

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  - Note the GPIO **bank** and **pin**.
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The GPIO **bank**, **pin**, **alternate function** and **HDPx multiplexing value** are the information required to configure each HDP signal.

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Refer to STM32MP15 reference manuals for the complete list of features, and to the software components, introduced below, to know which features are really implemented.

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The HDP is a **non-secure** peripheral.

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3.2.2 Software frameworks

Domain	Peripheral	Software components		Comment
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Trace & Debug	HDP		HDP Linux driver	

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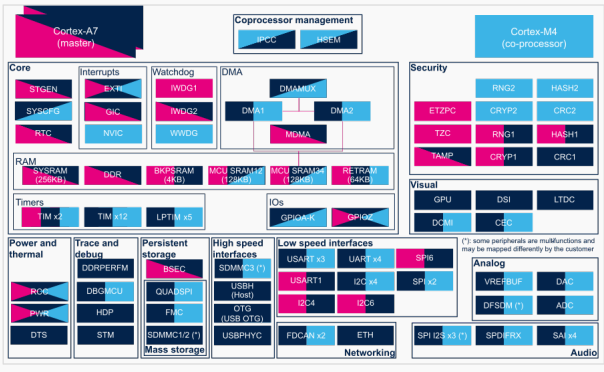

Internal peripherals

Domain	Periphera l	Runtime allocation		Comment
	Cortex-A7	Cortex-A7	Cortex-M4	

Domain	Periphera	Runtime allocation				Comment
Instance	secure (OP-TEE)	non-secure (Linux)	(STM32Cube)			
Trace & Debug	HDP	HDP				

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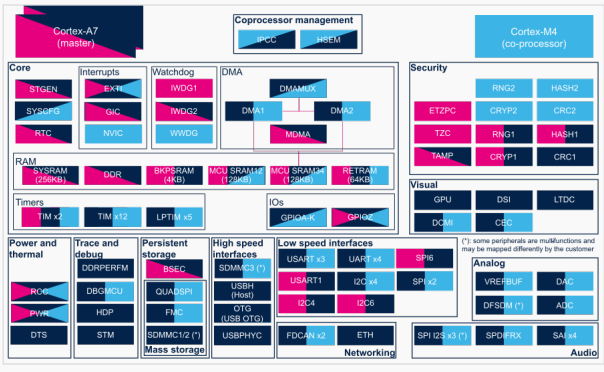

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Domain	Periphera	Runtime allocation				Comment
Instance	secure (OP-TEE)	non-secure (Linux)	(STM32Cube)			
Trace & Debug	HDP	HDP				

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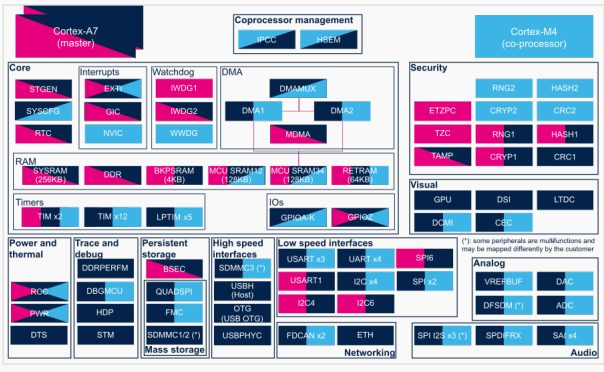

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Domain	Periphera	Runtime allocation				Comment
Instance	secure (OP-TEE)	non-secure (Linux)	(STM32Cube)			
Trace & Debug	HDP	HDP				

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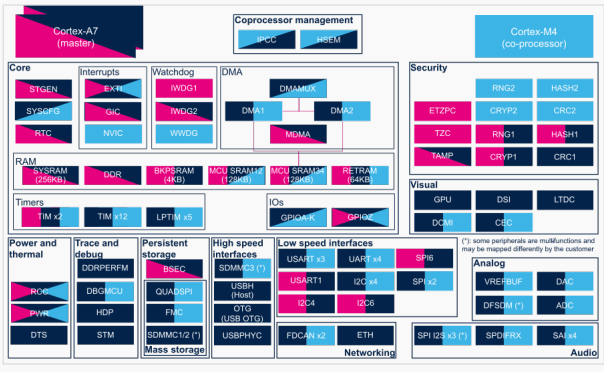

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Instance	secure (OP-TEE)	non-secure (Linux)	(STM32Cube)			
Trace & Debug	HDP	HDP				

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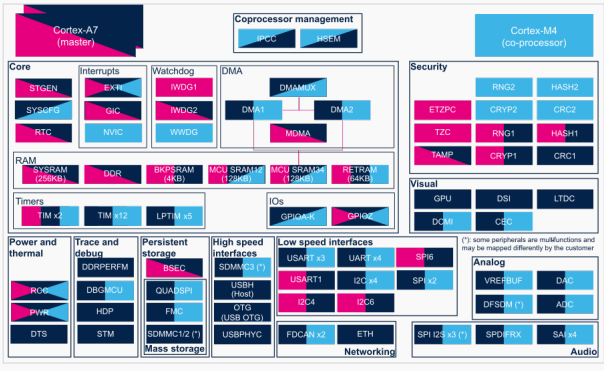

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Instance	secure (OP-TEE)	non-secure (Linux)	(STM32Cube)			
Trace & Debug	HDP	HDP				

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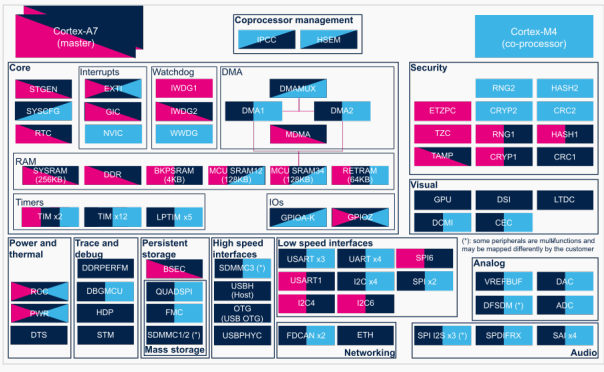

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Instance	secure (OP-TEE)	non-secure (Linux)	(STM32Cube)			
Trace & Debug	HDP	HDP				

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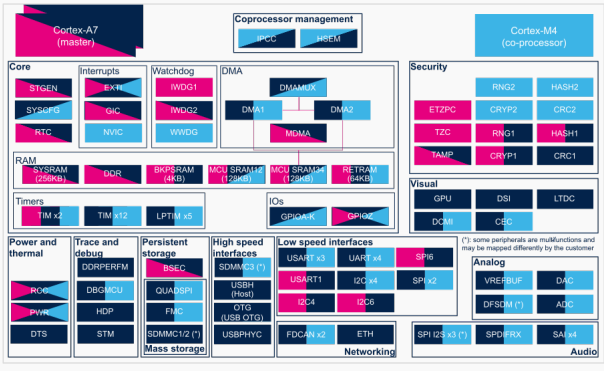

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