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HDP internal peripheral

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<noinclude> {{ArticleBasedOnModel| [[Internal peripheral article model]]}} {{ArticleMainWriter|GeraldB}} {{
ArticleApprovedVersion| GeraldB | AlexandreT, NathalieS |No previous approved version| AnneJ - 2Aug'18 - 8314
| 4Sep'18 }} [[Category:Trace and debug peripherals]] {{ReviewsComments|JCT 1840: alignment needed with the
last version of the model [[Internal peripheral article model]]<br> [[Category:ToBeAlignedWithModel]] }} <
/noinclude> ==Article purpose== The purpose of this article is to * briefly introduce the "HDP" peripheral
(hardware debug port) and its main features * indicate the level of security supported by this hardware block *
explain how each instance can be allocated to the three runtime contexts and linked to the corresponding software
components * explain, when needed, how to configure the HDP peripheral. ==Peripheral overview== The "HDP"
peripheral is used to output some internal signals on up to 8 [[GPIO internal peripheral|GPIO]] pins. <br /><br />
Follow the sequence below to connect a GPIO to an internal signal via the HDP: * First of all, look for the internal
signal you want to monitor in the HDP signal multiplexing table of the [[STM32MP15 resources#Reference
manuals|STM32MP15 reference manuals]]: ** Search for the HDP signal on which you can get it among eight
possible choices. ** Note the corresponding "HDPx multiplexing value" to select. * Then, look for the most
suitable [[GPIO internal peripheral|GPIO]] pin on which you can output HDPx in the [[STM32MP15
resources#STM32MP157Cxx data brief|data brief]]: ** Note the [[GPIO internal peripheral|GPIO]] "bank" and
"pin". ** Note the corresponding [[GPIO internal peripheral|GPIO]] "alternate function" (AF) to select. The [[GPIO
internal peripheral|GPIO]] "bank", "pin", "alternate function" and "HDPx multiplexing value" are the information
required to configure each HDP signal. <br /> ===Features=== Refer to [[STM32MP15 resources#Reference
manuals|STM32MP15 reference manuals]] for the complete list of features, and to the software components,
introduced below, to know which features are really implemented.<br> {{ReviewsComments|NSA W829 : it should
be good to add here with few words what are the main features provided by this internal peripheral}} ===Security
support=== The HDP is a "non-secure" peripheral. ==Peripheral usage and associated software== ===Boot
time=== The HDP is not used at boot time. ===Runtime=== =====Overview===== The HDP can be allocated to the
Arm<sup>&reg;</sup> Cortex<sup>&reg;</sup>-A7 non-secure core to be used under Linux<sup>&reg;</sup>
[[HDP Linux driver|HDP driver]]. =====Software frameworks===== {{:Internal_peripherals_software_table_template}}
| Trace & Debug | [[HDP internal peripheral|HDP]] | | [[HDP Linux driver|HDP Linux driver]] | | |- }} =====Peripheral
configuration===== The configuration is applied by the firmware running in the context to which the peripheral is
```

assigned. The configuration by itself can be performed via the [[STM32CubeMX]] tool for all internal peripherals. It can then be manually completed (especially for external peripherals) according to the information given in the corresponding software framework article. =====Peripheral assignment===== {{:Internal_peripherals_assignment_table_template}} <onlyinclude> | rowspan="1" | Trace & Debug | rowspan="1" | [[HDP internal peripheral|HDP]] | HDP | | □ | | |- </onlyinclude> |}

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