



File:STM32MP1IPsOverview.png

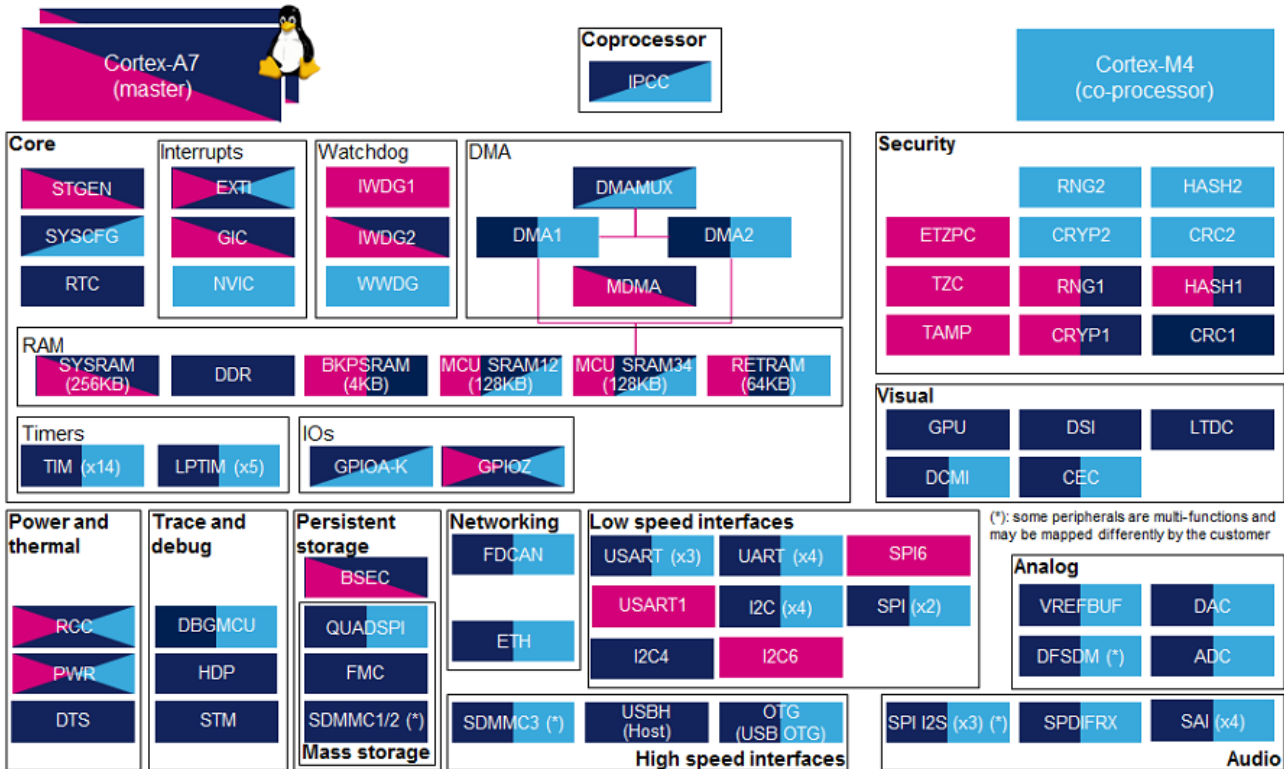
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Size of this preview: 800 × 477 pixels. Other resolutions: 320 × 191 pixels | 900 × 537 pixels.

Original file (900 × 537 pixels, file size: 139 KB, MIME type: image/png)

A quality version of this page, approved on 4 June 2021, was based off this revision.

Overview of all STM32MP1 internal peripherals

Source






STMicroelectronics




File history

Click on a date/time to view the file as it appeared at that time.

(newest | oldest) View (newer 10 | older 10) (10 | 20 | 50 | 100 | 250 | 500)

	Date/Time	Thumbnail	Dimensions	User	Comment
current	07:14, 4 June 2021		900 × 537 (139 KB)	Rald Baeza (talk contribs)	
	09:45, 10 November 2020		900 × 540 (140 KB)	Rald Baeza (talk contribs)	
	15:23, 27 August 2019		900 × 539 (182 KB)	Rald Baeza (talk contribs)	
	15:21, 27 August 2019		900 × 539 (182 KB)	Rald Baeza (talk contribs)	
	12:14, 23 April 2019		900 × 539 (190 KB)	Rald Baeza (talk contribs)	
	09:16, 29 October 2018		900 × 539 (184 KB)	FB08690 (talk contribs)	
	09:15, 29 October 2018		800 × 479 (151 KB)	FB08690 (talk contribs)	
	09:14, 29 October 2018		800 × 479 (151 KB)	FB08690 (talk contribs)	
	13:52, 2 October 2018		900 × 539 (172 KB)	FB08690 (talk contribs)	



	Date/Time	Thumbnail	Dimensions	User	Comment
	13:49, 16 August 2018		900 × 539 (173 KB)	Fb08690 (talk contribs)	

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- You cannot overwrite this file.



File usage

The following 64 pages link to this file:

- ADC internal peripheral
- BKPSRAM internal memory
- BSEC internal peripheral
- CEC internal peripheral
- CRC internal peripheral
- CRYP internal peripheral
- DAC internal peripheral
- DBGMCU internal peripheral
- DCMI internal peripheral
- DDRCTRL and DDRPHYC internal peripherals
- DDRPERFM internal peripheral
- DFSDM internal peripheral
- DMAMUX internal peripheral
- DMA internal peripheral
- DSI internal peripheral
- DTS internal peripheral
- ETH internal peripheral
- ETM internal peripheral
- ETZPC internal peripheral
- EXTI internal peripheral
- FDCAN internal peripheral
- FMC internal peripheral
- GIC internal peripheral
- GPIO internal peripheral
- GPU internal peripheral
- HASH internal peripheral
- HDP internal peripheral
- HSEM internal peripheral
- I2C internal peripheral
- IPCC internal peripheral
- IWDG internal peripheral
- Internal peripherals assignment table template
- LPTIM internal peripheral
- LTDC internal peripheral
- MCU SRAM internal memory
- MDMA internal peripheral
- NVIC internal peripheral
- OTG internal peripheral
- PWR internal peripheral
- QUADSPI internal peripheral



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- RCC internal peripheral
 - RETRAM internal memory
 - RNG internal peripheral
 - RTC internal peripheral
 - SAI internal peripheral
 - SDMMC internal peripheral
 - SPDIFRX internal peripheral
 - SPI internal peripheral
 - STGEN internal peripheral
 - STM32MP15 microprocessor
 - STM32MP15 peripherals overview
 - STM internal peripheral
 - SYSCFG internal peripheral
 - SYSRAM internal memory
 - TAMP internal peripheral
 - TIM internal peripheral
 - TSGEN internal peripheral
 - TZC internal peripheral
 - USART internal peripheral
 - USBH internal peripheral
 - USBPHYC internal peripheral
 - VREFBUF internal peripheral
 - WWDG internal peripheral
 - Contributors:Internal peripheral article model



Metadata

This file contains additional information, probably added from the digital camera or scanner used to create or digitize it. If the file has been modified from its original state, some details may not fully reflect the modified file.

Horizontal resolution 35.43 dpc

Vertical resolution 35.43 dpc

Software used

- www.inkscape.org