



DTS internal peripheral

DTS internal peripheral



Contents

1. DTS internal peripheral	3
2. Main Page	3



The content format pdf is not supported by the content model wikitext.

[Return to Main Page](#)

Stable: 17.11.2021 - 10:46 / Revision: 17.11.2021 - 15:58

You do not have permission to edit this page, for the following reasons:

- The action you have requested is limited to users in one of the groups: **Administrators**, **Editors**, **Reviewers**, Selected_editors, ST_editors.
- The action "Read pages" for the draft version of this page is only available for the groups ST_editors, ST_readers, Selected_editors, sysop, reviewer

You can view and copy the source of this page.

```
<noinclude> {{ArticleBasedOnModel| [[Internal peripheral article model]]}} {{ArticleMainWriter|OlivierB}}
{{ArticleApprovedVersion| GeraldB | NathalieS , OlivierB | No previous approved version | BrunoB - 21Sep'18 -
8855 | 12Oct'18 }} [[Category:Power and Thermal peripherals]] </noinclude> ==Peripheral overview== The "DTS"
peripheral is used to monitor the device temperature and take some preventive action (like frequency scaling or
peripheral disabling) in case it is becoming too high and before destroying the component.<br /> ==Features==
Refer to the [[STM32MP15 resources#Reference manuals|STM32MP15 reference manuals]] for the complete list
of features , and to the software components, introduced below, to see which features are implemented.<br>
==Security support== The DTS is a "non secure" peripheral. ==Peripheral usage and associated software==
==Boot time== DTS is not used at boot time. ==Runtime== ==Overview== The device cannot warm up
if the Cortex<sup>&reg;</sup>-M4 is running alone, as a consequence the monitoring is only done from the
Cortex-A7 non-secure context with Linux<sup>&reg;</sup> [[Thermal overview|thermal management framework]].
==Software frameworks== {{:Internal_peripherals_software_table_template}} | Power & Thermal | [[DTS
internal peripheral|DTS]] | | [[Thermal overview|Linux thermal framework]] | | - |} ==Peripheral
configuration== The configuration is applied by the firmware running in the context to which the peripheral is
assigned. The configuration can be done alone via the [[STM32CubeMX]] tool for all internal peripherals, and then
manually completed (particularly for external peripherals), according to the information given in the corresponding
software framework article. ==Peripheral assignment== {{:Internal_peripherals_assignment_table_template}}
<onlyinclude> | rowspan="1" | Power & Thermal | rowspan="1" | [[DTS internal peripheral|DTS]] | DTS | | <span
title="assignable peripheral" style="font-size:21px"></span> | | - </onlyinclude> |} ==References== <references/>
```

Templates used on this page:

- [Template:Highlight](#) (view source)
- [Template:Info](#) (view source)
- [Template:STDarkBlue](#) (view source)

[Return to Main Page](#).