



Coprocessor power management



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- [\(cur | prev\)09:11, 1 December 2020 m . . \(3,245 bytes\) \(+4\) \[Reviewed: 10:35, 1 December 2020 | \]](#)
- [\(cur | prev\)15:14, 20 February 2019 m . . \(3,140 bytes\) \(+24\) . . \(â Low power modes available on the chip\) \[Reviewed: 07:35, 22 February 2019 | | \[Expert: Aproved, Technical writer: Approved, Maintainer: Approved\]\]](#)
- [\(cur | prev\)06:54, 22 January 2019 m . . \(2,370 bytes\) **\(+2,370\)** \[Reviewed: 11:21, 11 February 2019 | | \[Expert: Aproved, Technical writer: Approved, Maintainer: Approved\]\]](#)