



Category:RAM interfaces

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A quality version of this page, approved on 17 June 2020, was based off this revision.

This category groups together all articles related to the **RAM interfaces** internal peripherals and memories (hardware blocks) embedded in the STM32 MPUs microprocessor devices.

Random Access Memory (Early computer memories generally had serial access. Memories where any given address can be accessed when desired were then called "random access" to distinguish them from the memories where contents can only be accessed in a fixed order. The term is used today for volatile random-access semiconductor memories.)



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