ADC internal peripheral

Contents

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A quality version of this page, accepted on *11 February 2019*, was based off this revision.

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Contents

1 Article purpose

The purpose of this article is to

- briefly introduce the ADC peripheral and its main features
- indicate the level of security supported by this hardware block
- explain how each instance can be allocated to the three runtime contexts and linked to the corresponding software components
- explain how to configure the ADC peripheral.

2 Peripheral overview

The STM32 **ADC** is a successive approximation analog-to-digital converter.

2.1 Features

The STM32MP15 has one ADC block with two physical ADCs:

- **Configurable resolution**: 8, 10, 12, 14, 16 bits.
- Each ADC has up to 20 **multiplexed channels** (including 6 internal channels connected only to ADC2).
- The conversions can be performed in **single, continuous, scan or discontinuous mode**.
- The result can be read in a left- or right-aligned 32-bit data register by using **CPU or DMA**[1].
- The **analog watchdog** feature allows the application to detect if the input voltage goes beyond the user-defined, high or low thresholds.
- A **common input clock** for the two ADCs, which can be selected between 2 different clock[2] sources (Synchronous or Asynchronous clock).
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Refer to STM32MP15 reference manuals for the complete features list, and to the software components, introduced below, to know which features are really implemented.

2.2 Security support

The ADC is a **non-secure** peripheral.

3 Peripheral usage and associated software

3.1 Boot time

The ADC is usually not used at boot time. But it may be used by the SSBL (see Boot chains overview), to check for power supplies for example.

3.2 Runtime

3.2.1 Overview

The ADC can be allocated to:

- the Arm®Cortex®-A7 non-secure core to be used under Linux® with IIO framework.

or

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The Peripheral assignment chapter describes which peripheral instance can be assigned to which context.

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The configuration is applied by the firmware running in the context to which the peripheral is assigned. The configuration by itself can be performed via the STM32CubeMX tool for all internal peripherals. It can then be manually completed (especially for external peripherals) according to the information given in the corresponding software framework article.

For the Linux kernel configuration, please refer to ADC device tree configuration and ADC Linux driver articles.

3.2.4 Peripheral assignment

Check boxes illustrate the possible peripheral allocations supported by STM32 MPU Embedded Software:

- means that the peripheral can be assigned () to the given runtime context.
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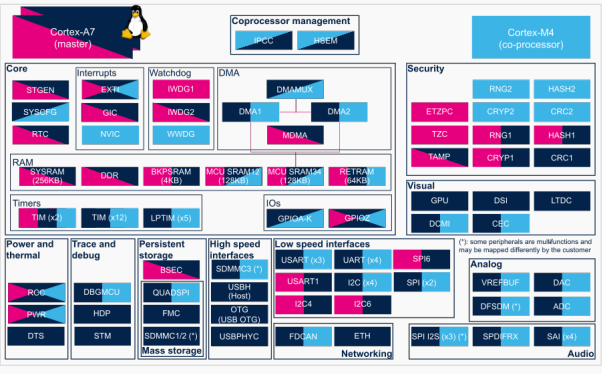

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Analog	ADC	ADC				Assignment (single choice)

4 How to go further

See the application note:

- How to get the best ADC accuracy in STM32[8].

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Central processing unit

Direct Memory Access

voltage reference buffer (STM32 specific)

low-power timer (STM32 specific)

External Interrupt

Second Stage Boot Loader

Microprocessor Unit

Open Portable Trusted Execution Environment

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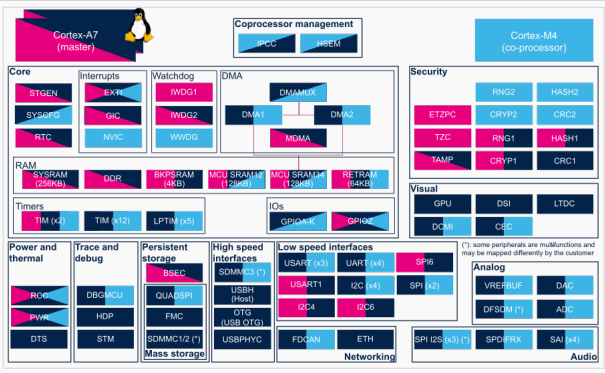

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Second Stage Boot Loader

Microprocessor Unit

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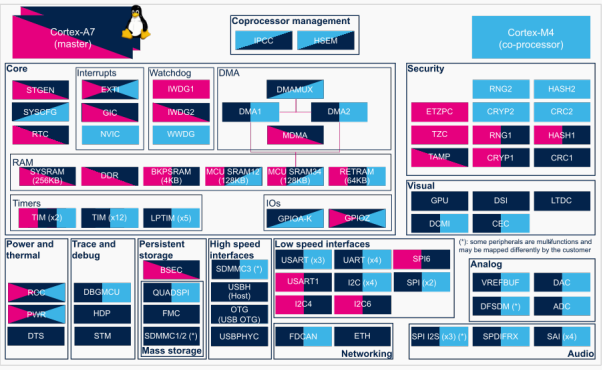

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External Interrupt

Second Stage Boot Loader

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Open Portable Trusted Execution Environment

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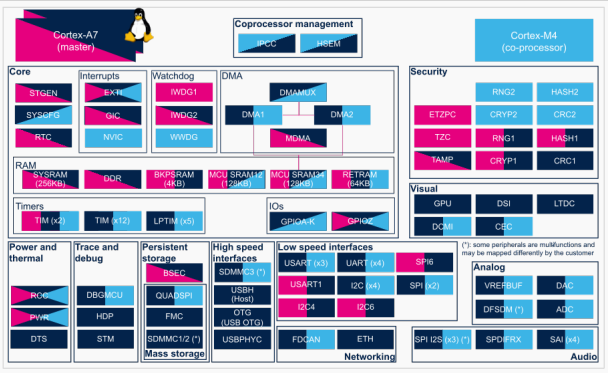

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External Interrupt

Second Stage Boot Loader

Microprocessor Unit

Open Portable Trusted Execution Environment

Stable: 13.10.2020 - 08:29 / Revision: 13.10.2020 - 08:29

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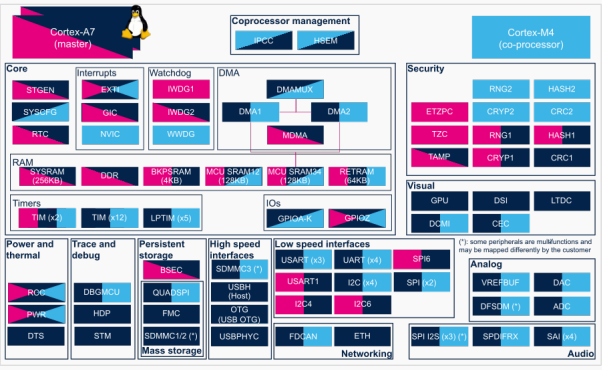

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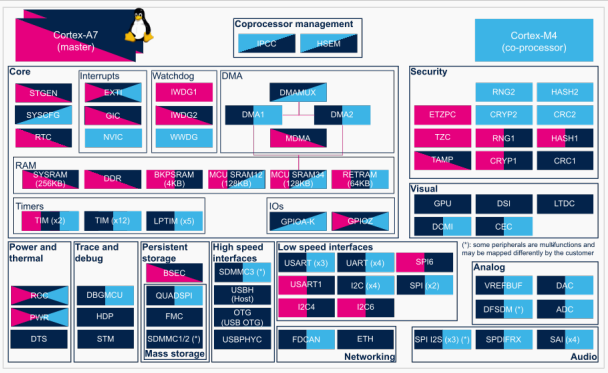

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Cortex®

Microprocessor Unit

Open Portable Trusted Execution Environment

Stable: 08.03.2021 - 16:13 / Revision: 16.02.2021 - 17:11

Template:ArticleMainWriter Template:ArticleApprovedVersion

Contents

1 Article purpose

The purpose of this article is to

- briefly introduce the ADC peripheral and its main features
- indicate the level of security supported by this hardware block
- explain how each instance can be allocated to the three runtime contexts and linked to the corresponding software components
- explain how to configure the ADC peripheral.

2 Peripheral overview

The STM32 **ADC** is a successive approximation analog-to-digital converter.

2.1 Features

The STM32MP15 has one ADC block with two physical ADCs:

- **Configurable resolution**: 8, 10, 12, 14, 16 bits.
- Each ADC has up to 20 **multiplexed channels** (including 6 internal channels connected only to ADC2).
- The conversions can be performed in **single, continuous, scan or discontinuous mode**.
- The result can be read in a left- or right-aligned 32-bit data register by using **CPU or DMA**[1].
- The **analog watchdog** feature allows the application to detect if the input voltage goes beyond the user-defined, high or low thresholds.
- A **common input clock** for the two ADCs, which can be selected between 2 different clock[2] sources (Synchronous or Asynchronous clock).
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Each ADC supports two contexts to manage conversions:

- **Regular conversions** can be done in sequence, running in background
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- Each context has its own **configurable sequence and trigger**: software, TIM[5], LPTIM[6] and EXTI[7].

Refer to STM32MP15 reference manuals for the complete features list, and to the software components, introduced below, to know which features are really implemented.

2.2 Security support

The ADC is a **non-secure** peripheral.

3 Peripheral usage and associated software

3.1 Boot time

The ADC is usually not used at boot time. But it may be used by the SSBL (see Boot chains overview), to check for power supplies for example.

3.2 Runtime

3.2.1 Overview

The ADC can be allocated to:

- the Arm®Cortex®-A7 non-secure core to be used under Linux® with IIO framework.

or

- the Arm®Cortex®-M4 to be used with STM32Cube MPU Package with ADC HAL driver.

The Peripheral assignment chapter describes which peripheral instance can be assigned to which context.

3.2.2 Software frameworks

Domain	Peripheral	Software components			Comment
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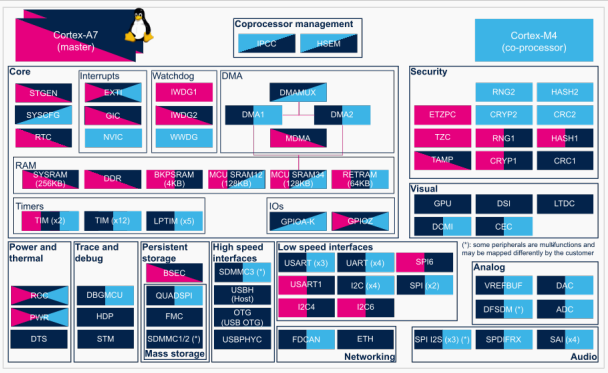
The configuration is applied by the firmware running in the context to which the peripheral is assigned. The configuration by itself can be performed via the STM32CubeMX tool for all internal peripherals. It can then be manually completed (especially for external peripherals) according to the information given in the corresponding software framework article.

For the Linux kernel configuration, please refer to ADC device tree configuration and ADC Linux driver articles.

3.2.4 Peripheral assignment

Check boxes illustrate the possible peripheral allocations supported by STM32 MPU Embedded Software:

- means that the peripheral can be assigned () to the given runtime context.
- is used for system peripherals that cannot be unchecked because they are statically connected in the device.



Internal peripherals

Refer to How to assign an internal peripheral to a runtime context for more information on how to assign peripherals manually or via STM32CubeMX.
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Analog	ADC	ADC				Assignment (single choice)

4 How to go further

See the application note:

- How to get the best ADC accuracy in STM32[8].

5 References

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Central processing unit

Direct Memory Access

voltage reference buffer (STM32 specific)

low-power timer (STM32 specific)

External Interrupt

Second Stage Boot Loader

Microprocessor Unit

Open Portable Trusted Execution Environment

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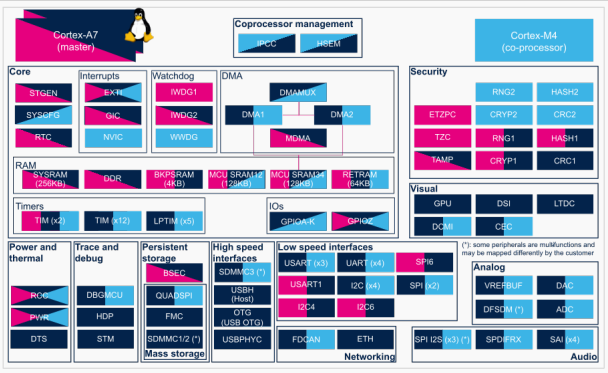

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low-power timer (STM32 specific)

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Second Stage Boot Loader

Cortex®

Microprocessor Unit

Open Portable Trusted Execution Environment

Stable: 30.03.2021 - 13:25 / Revision: 29.03.2021 - 09:46

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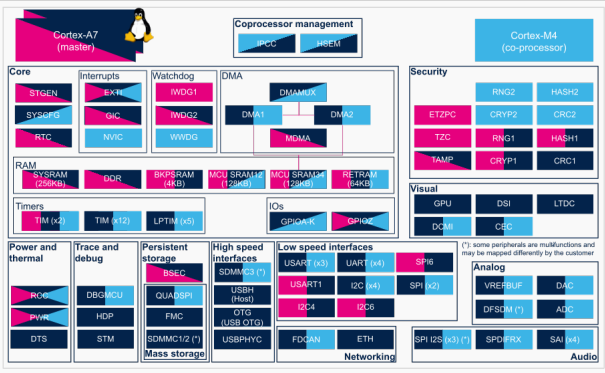

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Second Stage Boot Loader

Cortex®

Microprocessor Unit

Open Portable Trusted Execution Environment

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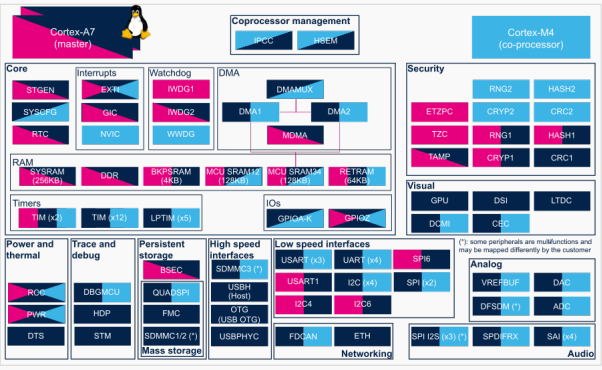

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Second Stage Boot Loader

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Cortex®

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Microprocessor Unit

Open Portable Trusted Execution Environment

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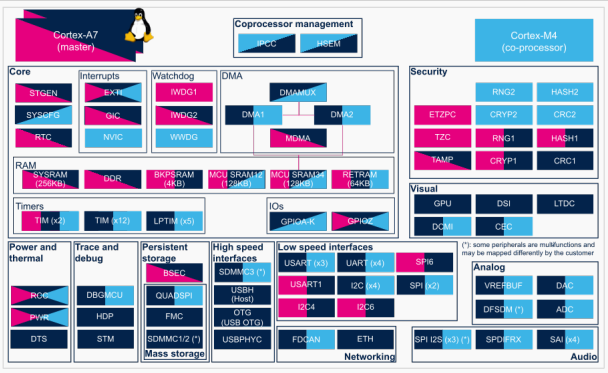

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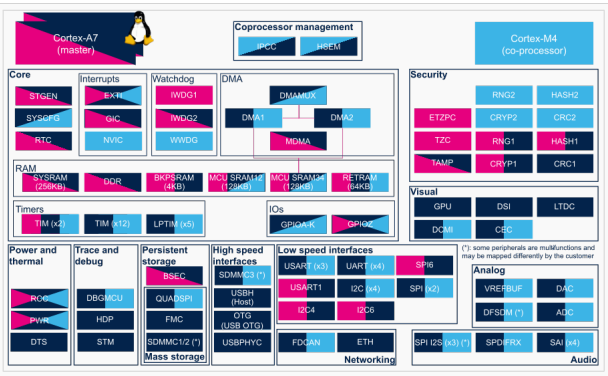

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External Interrupt

Second Stage Boot Loader

Cortex®

Microprocessor Unit

Open Portable Trusted Execution Environment

Stable: 23.09.2020 - 13:22 / Revision: 12.06.2020 - 13:25

Template:ArticleMainWriter Template:ArticleApprovedVersion

Contents

1 Article purpose

The purpose of this article is to

- briefly introduce the ADC peripheral and its main features
- indicate the level of security supported by this hardware block
- explain how each instance can be allocated to the three runtime contexts and linked to the corresponding software components
- explain how to configure the ADC peripheral.

2 Peripheral overview

The STM32 **ADC** is a successive approximation analog-to-digital converter.

2.1 Features

The STM32MP15 has one ADC block with two physical ADCs:

- **Configurable resolution**: 8, 10, 12, 14, 16 bits.
- Each ADC has up to 20 **multiplexed channels** (including 6 internal channels connected only to ADC2).
- The conversions can be performed in **single, continuous, scan or discontinuous mode**.
- The result can be read in a left- or right-aligned 32-bit data register by using **CPU or DMA**[1].
- The **analog watchdog** feature allows the application to detect if the input voltage goes beyond the user-defined, high or low thresholds.
- A **common input clock** for the two ADCs, which can be selected between 2 different clock[2] sources (Synchronous or Asynchronous clock).
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- Each context has its own **configurable sequence and trigger**: software, TIM[5], LPTIM[6] and EXTI[7].

Refer to STM32MP15 reference manuals for the complete features list, and to the software components, introduced below, to know which features are really implemented.

2.2 Security support

The ADC is a **non-secure** peripheral.

3 Peripheral usage and associated software

3.1 Boot time

The ADC is usually not used at boot time. But it may be used by the SSBL (see Boot chains overview), to check for power supplies for example.

3.2 Runtime

3.2.1 Overview

The ADC can be allocated to:

- the Arm®Cortex®-A7 non-secure core to be used under Linux® with IIO framework.

or

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For the Linux kernel configuration, please refer to ADC device tree configuration and ADC Linux driver articles.

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Check boxes illustrate the possible peripheral allocations supported by STM32 MPU Embedded Software:

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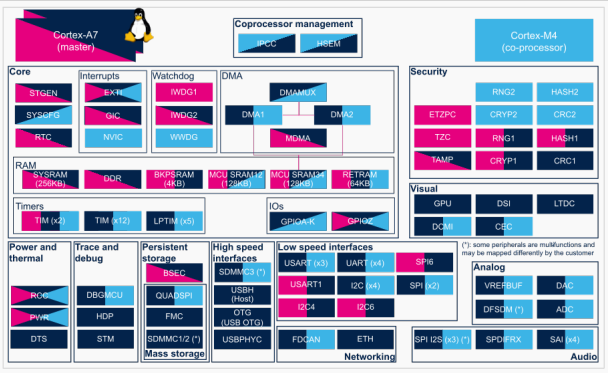

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Analog	ADC	ADC				Assignment (single choice)

4 How to go further

See the application note:

- How to get the best ADC accuracy in STM32[8].

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low-power timer (STM32 specific)

External Interrupt

Second Stage Boot Loader

Microprocessor Unit

Open Portable Trusted Execution Environment

Stable: Not stable / Revision: 11.03.2021 - 08:07

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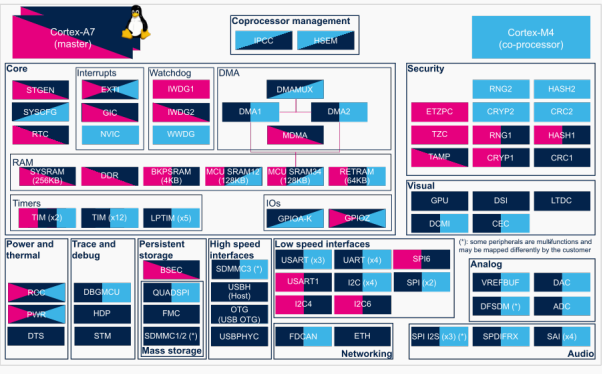

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Second Stage Boot Loader

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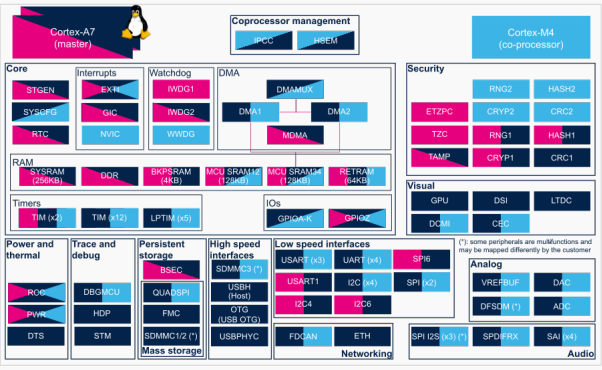

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Cortex®

Microprocessor Unit

Open Portable Trusted Execution Environment

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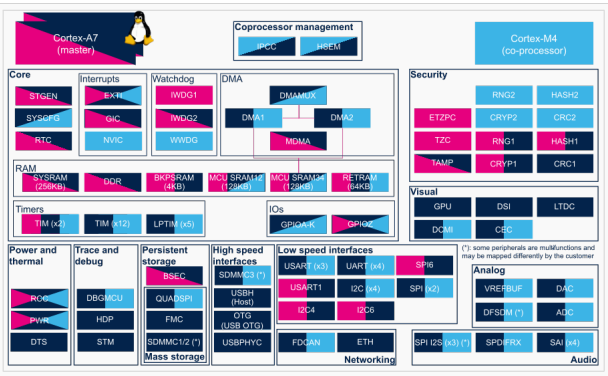

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Stable: 25.09.2020 - 09:43 / Revision: 25.09.2020 - 09:37

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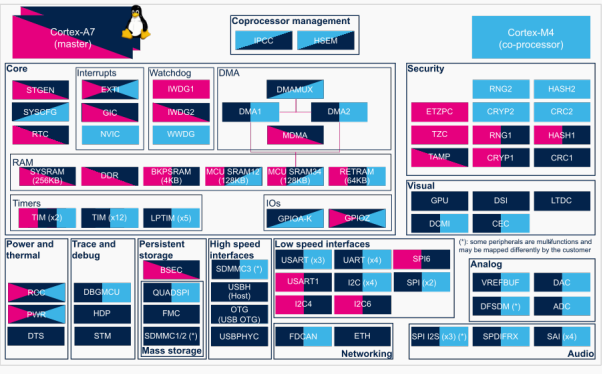

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